



*Handwritten:* #10-11-01

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. .... 09/756,971  
Filing Date ..... January 9, 2001  
Inventor ..... Akram  
Assignee ..... Micron Technology, Inc.  
Group Art Unit ..... 2826  
Examiner ..... Unknown  
Attorney's Docket No. .... MI22-1572  
Title: Methods of Forming Board-On-Chip Packages

**SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT**

References -- See Attached Form PTO-1449

The Examiner's attention is directed to the references which are listed on the attached Form PTO-1449, copies of which are attached. No admission is made regarding whether all the submitted references are prior art.

Citation of the referenced art is respectfully requested.

This Information Disclosure Statement is being filed within three months of the filing date of the application or before the mailing of a first Office Action, whichever occurs last. Therefore, no fee is believed to be required. However, in the event that a fee is required for filing this information disclosure statement, please charge the fee specified under 37 C.F.R. § 1.17(p) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Dated: 9/17/01

Respectfully submitted,

By: David G. Latwesen

David G. Latwesen, Ph.D.  
Reg. No. 38,533

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	Filing Date	January 9, 2001
	First Named Inventor	Salman Akram
	Group Art Unit	2826
	Examiner Name	Unknown
Total Number of Pages in This Submission		Attorney Docket Number MI22-1572

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Firm or Individual name	David G. Latwesen, Ph.D.; Wells, St. John et al.	
Signature		
Date	9/17/01	

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231 on this date: 9-17-2001			
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Signature		Date	9/17/01

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